

FIG. 11

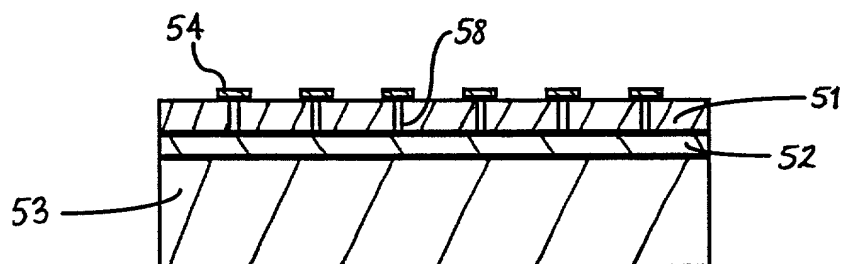


FIG. 12

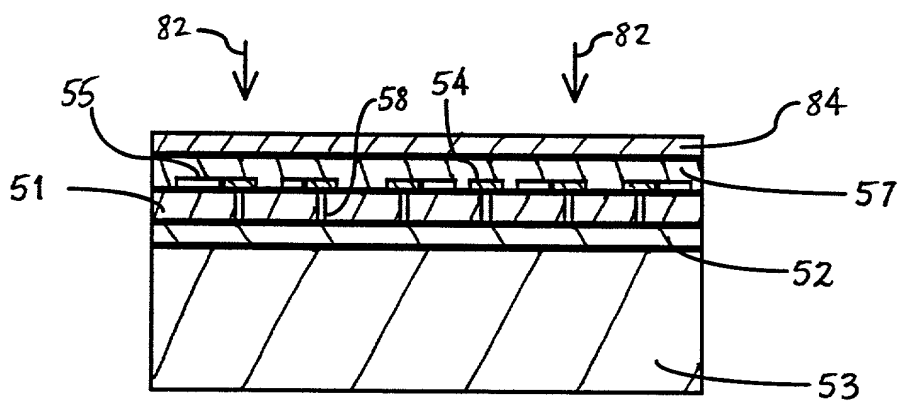


FIG. 13

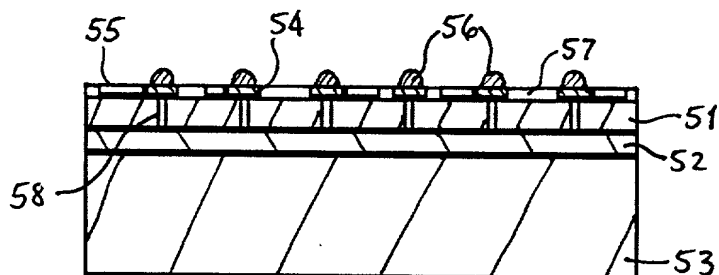


FIG. 1

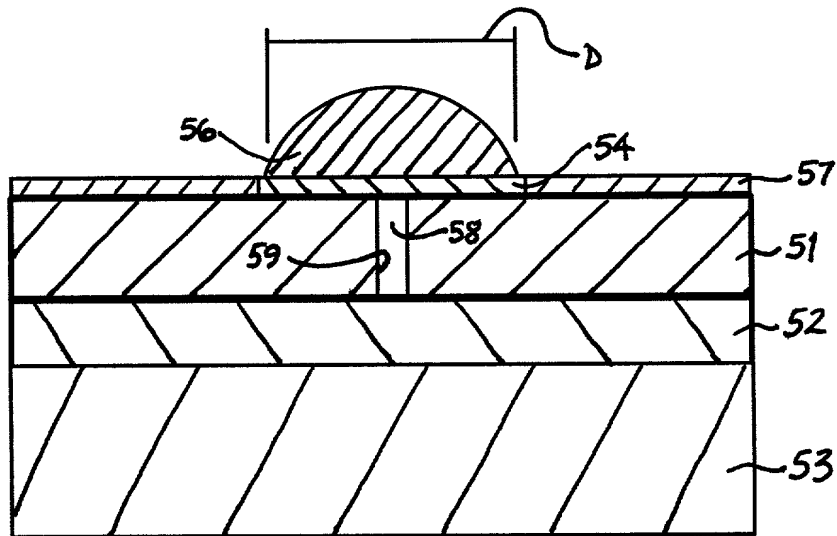
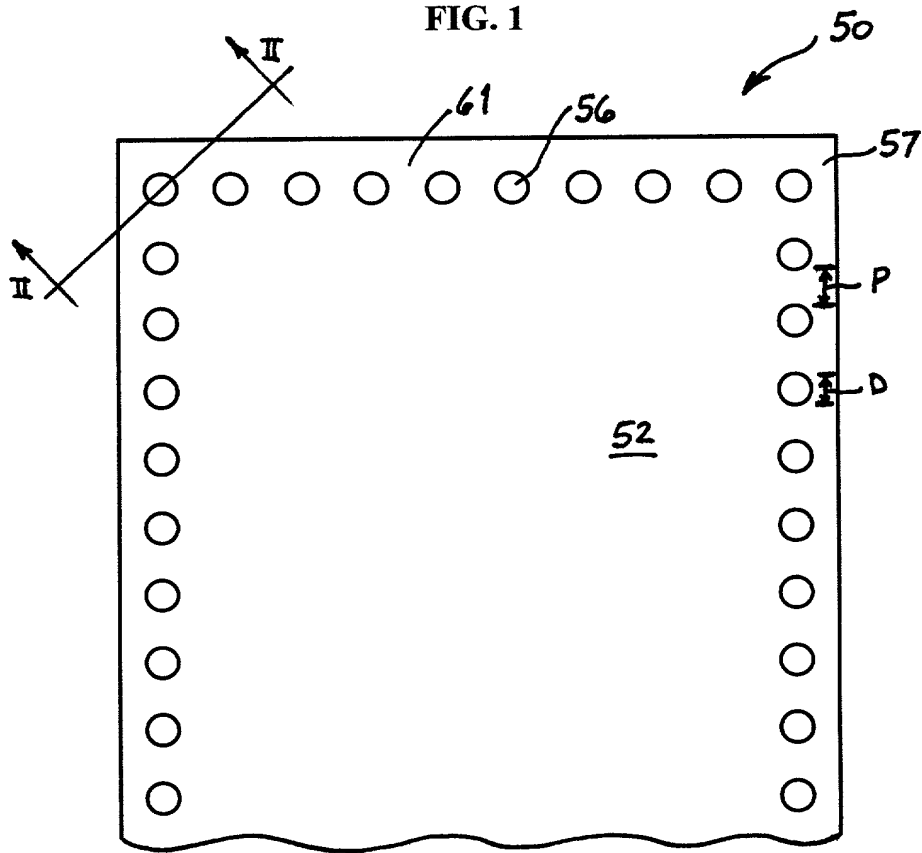


FIG. 2

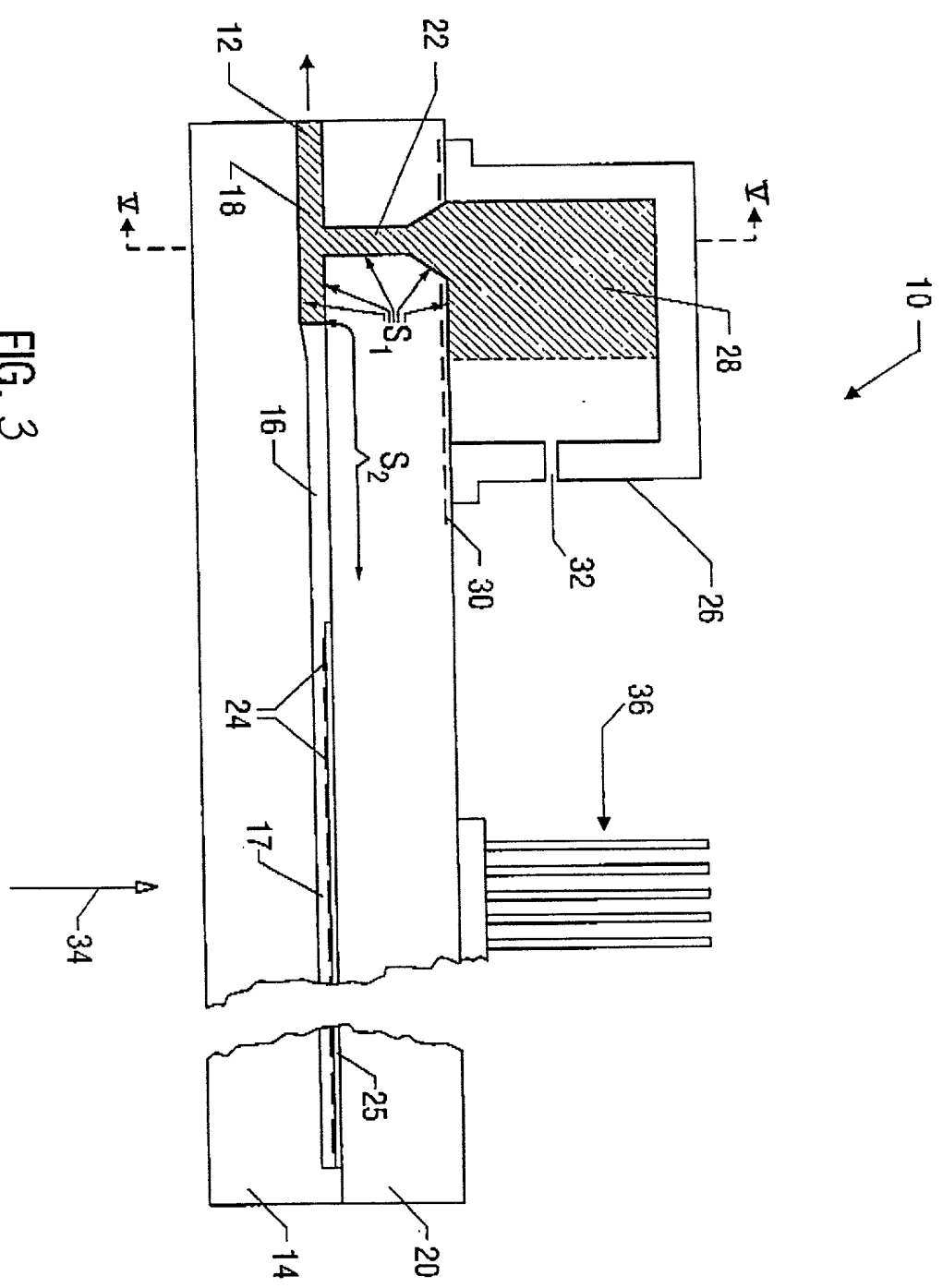


FIG. 3

FIG. 3 is a cross-sectional diagram of a device 10. The device includes a substrate 12 with a layer 14 on its top surface. A patterned layer 16 is formed on the substrate 12, defining a central feature 20. A top layer 18 is formed on the substrate 12, covering the patterned layer 16. The central feature 20 contains a layer 22 and a patterned layer 24. A side view 34 shows a layer 25. A cross-section 36 shows a series of vertical lines.

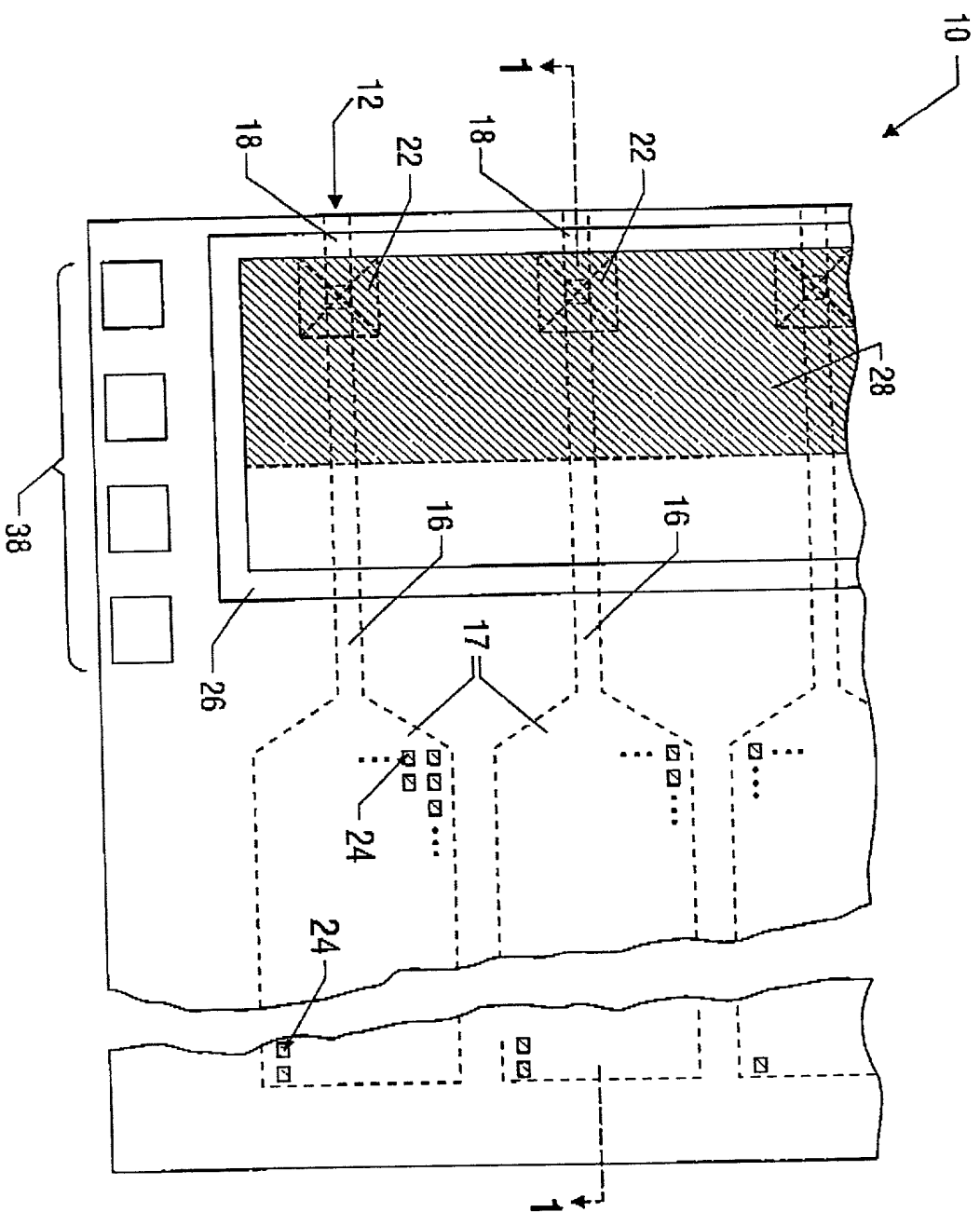


FIG. 4

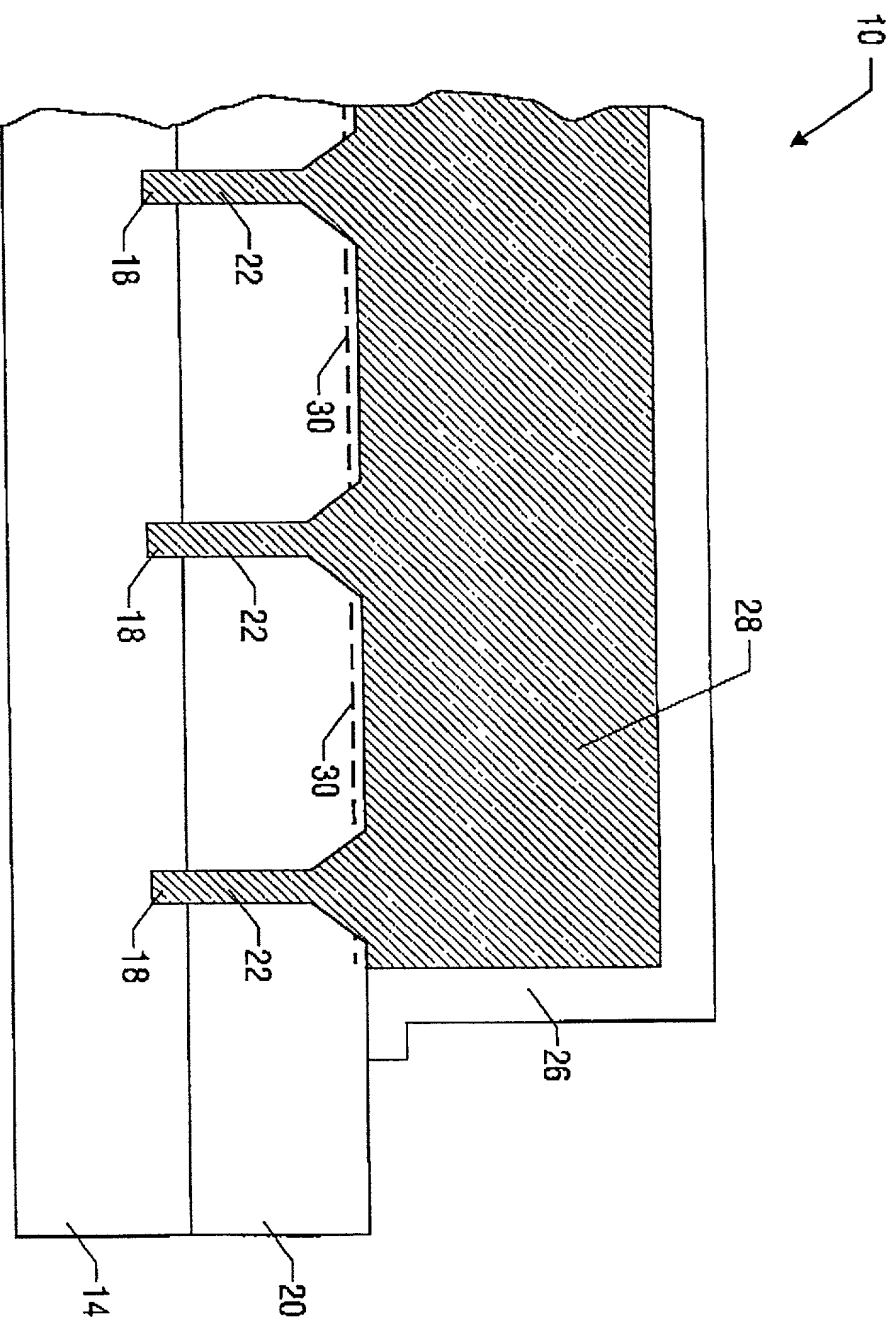


FIG. 5

FIG. 6

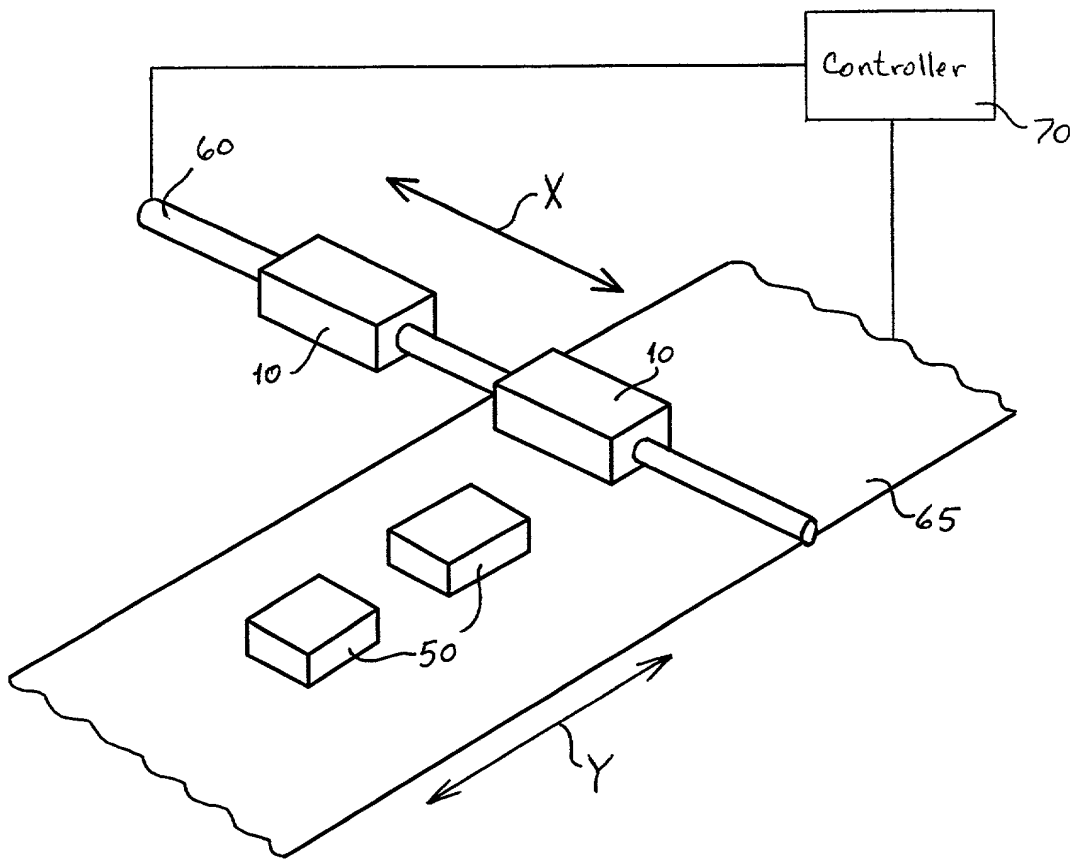


FIG. 7

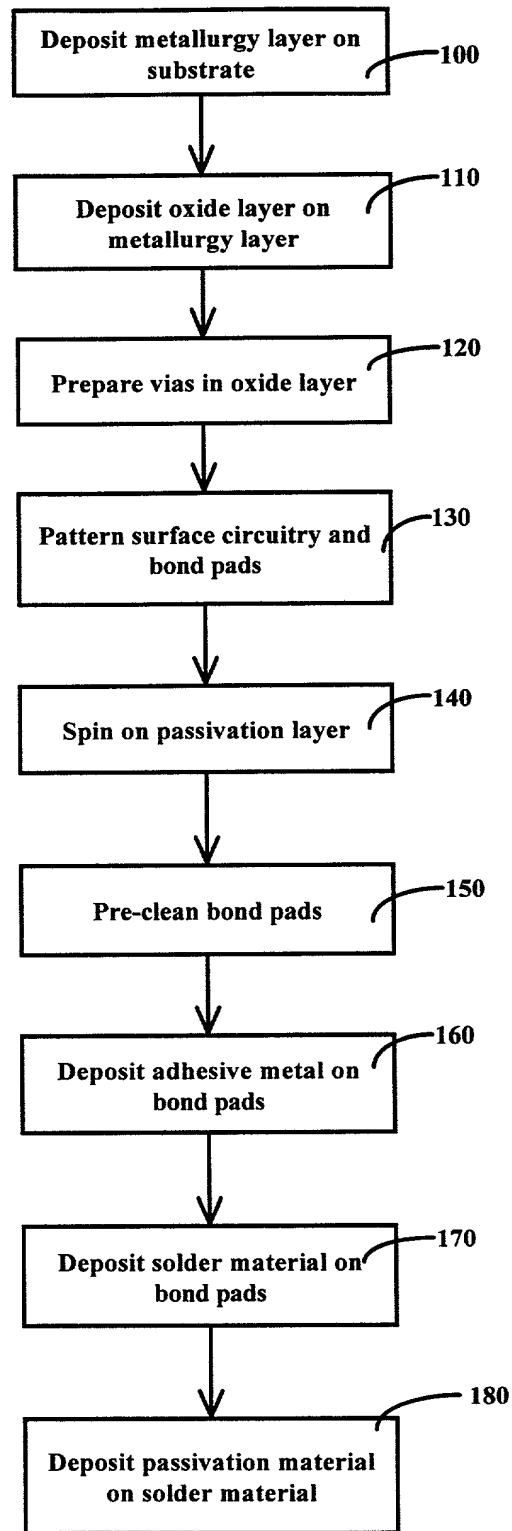


FIG. 8

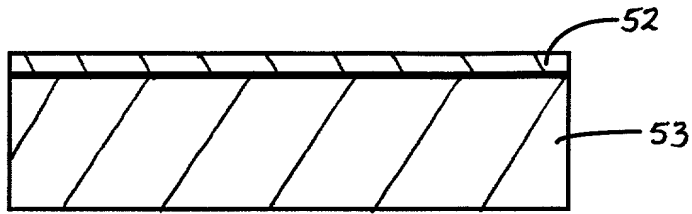


FIG. 9

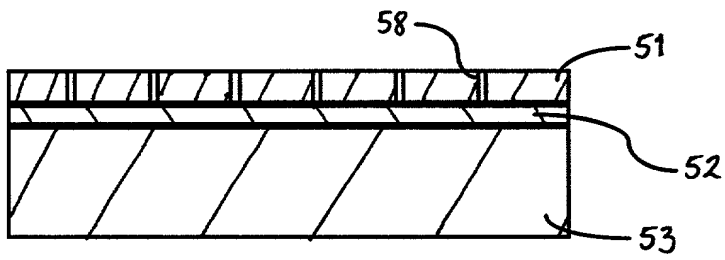


FIG. 10

